IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tran

Attorney Docket: TI-36252

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

Non-Uniform Decoupling Capacitor Distribution for Providing More Uniform Noise

Reduction Across Chip

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Respectfully submitted,

Barry W. Dove

Pamfore

Attorney for Applicant

Reg. No. 45,862

Slater & Matsil, L.L.P. 17950 Preston Rd., Suite 1000 Dallas, TX 75252 (972) 732-1001 (phone) (972) 732-9218 (fax)

Approved for use through 05/31/2003. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to

			persons are required to	copolid to a collection of informatio	on unless it displays a valid OMB control number	
Substitute for form 1449A/PTO				Complete if Known		
				Application Number	TBD	
į į	INFORMATION	1 DISCL	OSURE	Filing Date	Herewith	
STATEMENT BY APPLICANT				First Named Inventor	Tran	
				Art Unit	TBD	
(use as many sheets as necessary)			essary)	Examiner Name	TBD	
Sheet	1	of	1	Attorney Docket Number	TI-36252	

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ^{2 (if known)}			
		US-			
	-	US-			
		US-	***************************************		

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ°

OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite ₁ No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	-7²		
	A	JUNG, S., et al., "Packaging Consideration for 1 Gbps Si CMOS Optical Driver," IEEE (2000) pp. 882-883.			
	В	FAN, J., et al., "Quantifying Decoupling Capacitor Location," IEEE (February 2000) pp. 761-766.			
	С	PEIFFER, J., et al., "Electrical Performance Advantages of Ultra-Thin Dielectric Materials Used for Power-Ground Cores in High-Speed, Multilayer Printed Circuit Boards," Manuscript of Paper for IPC Expo 2003 (March 25-27, 2003) pp. 1-13.			

Examiner	Date	
	Date	
Signature	1 ^	
Oignature	I Considered	
G1444411		

Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. *Applicant is to place a check mark here if English language Translation is attached.